

**AMENDMENTS TO THE SPECIFICATION:**

Replace the paragraph beginning at page 4, line 11, with the following amended paragraph:

However, in the second transmission data DATA\_2B, DATA\_1B, DATA\_0B, the data reading timing [[401]] 402 precedes the transition point in the transmission data DATA\_2B whereas it falls after the transition point in the transmission data DATA\_1B and DATA\_0B. Consequently, the databits of the reception data DATA\_2C, DATA\_1C, and DATA\_0C become "111", as compared to the databit [[["010"]]] "011" of the transmitting end. As a result, the receiver receives an incorrect data and consequently performs an incorrect operation.

Replace the paragraph beginning at page 11, line 11, with the following amended paragraph:

The transmitter 10a includes transmission flip-flops 11a, [[12a]] 11b, and 11c, which carry out a synchronous operation according to the transmit clock CLOCK\_A, and control flip-flops 12a and 12b. Transmission data DATA\_2, DATA\_1, and DATA\_0 are input into the transmission flip-flops 11a, [[12a]] 11b, and 11c. The transmission flip-flops 11a, [[12a]] 11b, and 11c synchronize and output transmission data DATA\_2A, DATA\_1A, and DATA\_0A, which are then transmitted to the signal transmission lines 30a, 30b and 30c.